

Simulation Software $R\&S^{\mathbb{R}}WinIQSIM^{TM}$

... ideal for the generation of digitally modulated signals

- Calculation of digitally modulated I/Q and IF signals
- For driving the internal arbitrary waveform generator of the R&S SMU (R&S SMU-B10), the R&S SMIQ (R&S SMIQB60) and the I/Q Modulation Generator R&S AMIQ
- Single-carrier, multicarrier, multicarrier mixed signals and CDMA signals
- 3GPP FDD mode including data sets for the test models to 3GPP
- 3GPP TDD mode optional (R&S SMU-K13/R&S SMIQK13/AMIQK13)
- TD-SCDMA optional (R&S SMU-K14/ R&S SMIQK14/R&S AMIQK14)
- IS-95 CDMA optional (R&S SMU-K11/ R&S SMIQK11/R&S AMIQK11)
- cdma2000 optional (R&S SMU-K12/ R&S SMIQK12/R&S AMIQK12)
- Versatile data editor

- Superposition/simulation of impairments
- Graphical display
- Can be enhanced by import interface for additional software
- 1xEV-D0 optional (R&S SMU-K17/R&S SMIQK17/ R&S AMIQK17)
- IEEE 802.11 (a,b,g) optional (R&S SMU-K19/R&S SMIQK19/ R&S AMIQK19)



It has never been so easy

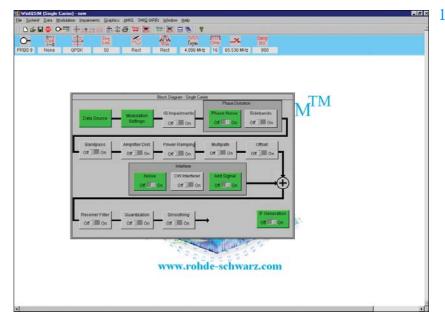
R&S WinIQSIM[™] was especially developed for the generation of digitally modulated signals. Complex signals can thus easily be generated. The graphical user interface allows intuitive operation, supported by context-sensitive help. The convenient way of creating any TDMA frame configurations with the aid of a data editor, and the generation of multicarrier signals as well as of complex WCDMA signals make R&S WinIQSIM[™] suitable for a wide range of applications. Moreover, additive impairments can be superimposed on a signal.

The signals generated with the aid of the R&S WinIQSIM[™] software can be output by the integrated solution in the R&S SMU (option R&S SMU-B10) and the R&S SMIQ (option R&S SMIQB60) as well as the Arbitrary Waveform Generator R&S AMIQ. R&S WinIQSIM[™] is provided with these three arbitrary waveform generators free of charge.

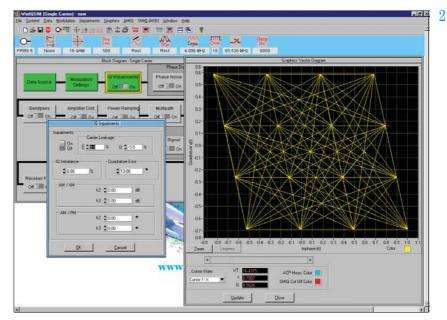
Install it and go ahead (1)

In developing R&S WinIQSIM[™], great importance was attached to user-friendly operation. The main parameters of a signal, for example, are indicated in a status line. The context-sensitive online help enables handling of even complex functions without consulting the manual.

The program always starts with the settings of the previous session, thus ensuring easy continuation of work.



Clearly structured menus in the form of a signal flow chart



Simulation of I/Q impairments, here for 16QAM

Single carrier (2, 3)

Modulation parameters such as type of modulation, coding, symbol rate, filter and window functions as well as oversampling can be set for a single-carrier signal. Impairments which may be caused by a real I/Ω modulator are also taken into consideration. It is, for example, easy to simulate I/Ω imbalance, carrier leakage or quadrature error. The simulation of VCO noise or phase and frequency offsets of

an oscillator are some of the very special features of R&S WinIQSIM[™]. These and many other settings enable the user to take real impairments into account early in the development phase of components and modules.

WCDMA, CDMA (4 to 12)

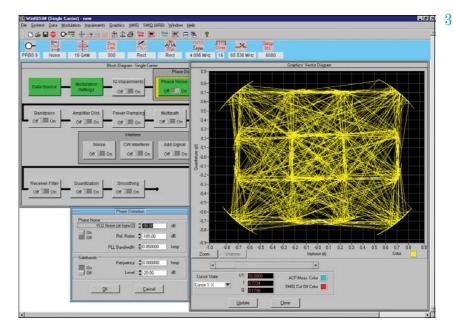
The comprehensive functionality of R&S WinIQSIM[™] allows various WCDMA systems to be realized: for example, both modes of the 3GPP standard, FDD (frequency division duplex) and TDD (time division duplex), are implemented. Signals can likewise be generated in accordance with the TD-SCDMA standard. And the North-American standards cdma2000 and cdma0ne are also included in R&S WinIQSIM[™].

All data and control channels defined by the relevant standard are supported. These include the synchronization channels such as primary and secondary common control channel (P-CCPCH and S-CCPCH) or synchronization channels (P-SCH and S-PCH).

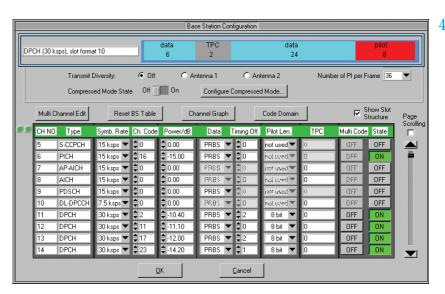
For the WCDMA standards as well as for the cdmaOne and cdma2000 standards, the orthogonal codes, data sources (PRBS, pattern or user-programmable sequences), and the power of the individual code channels can be varied, so that a large variety of signals can be generated.

R&S WinIQSIM[™] provides various display modes for visualizing the settings. The code domain display shows the distribution and occupancy of the individual channels in the code domain. Any code domain conflicts can be automatically resolved by a click. The channel graph includes all active channels. Synchronization and special channels are shown in red; data channels in green.

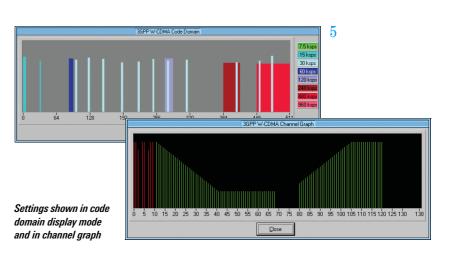
For statistical evaluation of the CDMA signal characteristics, R&S WinIQSIM[™] allows the complementary cumulative distribution function (CCDF) to be calculated (including the crest factor) and graphically displayed. In addition, the resulting adjacent-channel power can be calculated.



Simulation of defined phase noise on a 160AM-modulated signal



Definition of a code channel scenario for 3GPP FDD mode



Depending on the selected symbol rate, up to 512 code channels with a chip rate of 3.84 Mcps are generated in the FDD mode for testing base stations under realistic as well as under worst-case conditions.

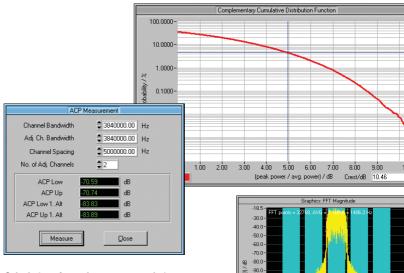
For this purpose, signals are generated which contain up to four mobile or base stations with different scrambling codes. R&S WinIQSIM[™] also allows the power of the individual data channels to be varied via TPC (transmit power control), which is used to control the power of the different channels in line with the 3GPP standard.

R&S WinIQSIM[™] supports the antenna diversity schemes specified by the 3GPP standard. Either the specification for antenna 1 or that for antenna 2 can be used so that the signal will be generated in line with the 3GPP specification.

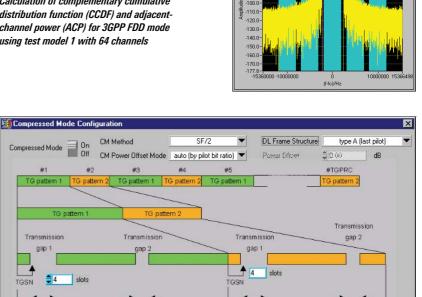
In the downlink, not only DPCHs (dedicated physical channels) are available as data channels but also HS-PDSCHs (high speed physical downlink shared channels) with the modulation modes QPSK and 160AM for HSDPA (high speed downlink packet access).

In the uplink, the mobile station can operate in one of the three permitted modes: PRACH only (physical random access channel), PCPCH only (physical common packet channel) and DPCCH + DPDCH (dedicated physical control channel and dedicated physical data channel).

The versatile settings enable even very specific tests to be carried out. For 3GPP FDD, for example, the compressed mode is supported, which allows handover of a mobile station from a 3GPP FDD base station to a base station (3GPP FDD, 3GPP TDD or GSM) with a different frequency. For this purpose, transmission and reception of the 3GPP FDD signal has to be interrupted for a certain time. In this transmission gap, the mobile station can



Calculation of complementary cumulative distribution function (CCDF) and adiacentchannel power (ACP) for 3GPP FDD mode using test model 1 with 64 channels



Cancel

slots

frames

OK

Editing of compressed mode

Transmission

1

slots

TGD1 🚔 24

slots

TGPL1 🚔 4

change to the frequency of the potential new base station in order to read, for example, the system information or the receive level of this base station. To allow the same data quantity to be transmitted in the remaining shorter time, data is compressed. R&S WinIQSIM[™] allows extensive user-defined settings for all physical layer compressed mode parameters.

In the TDD mode of the 3GPP standard, the link directions of the individual slots can be conveniently selected. The user can define whether each timeslot is to act as an uplink or a downlink.

slots

TGD2 📥 15

TGPL2

slots

frames

Up to four cells with 15 timeslots each can be generated; different spreading factors are permitted for each channel. For the data channels (DPCH), all spreading factors permitted by the standard are available.

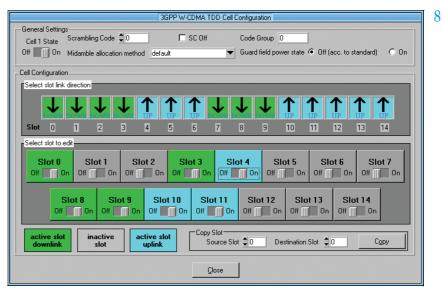
7

stots

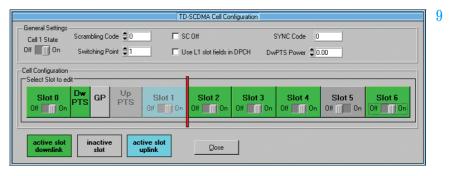
In the TDD mode, it is very important to calculate the CCDF not only for the total signal, but also for a specific timeslot. Since the system is made up of timeslots that can be switched on or off independently of one another, only the CCDF of an active slot is often of interest. This can then be used, for example, to optimally design the output amplifier of a mobile phone, since the latter is active in one slot only.

TD-SCDMA is basically similar to the 3GPP TDD mode. The two modes differ in the chip rate, which is 1.28 Mcps for TD-SCDMA instead of 3.84 Mcps in the TDD mode. According to the TD-SCDMA standard, the link direction of the individual slots cannot be selected by the user as conveniently as in the 3GPP TDD mode, a fact that has been taken into account by R&S WinIQSIM[™]. With TD-SCDMA, special timeslots are provided for the uplink and the downlink. To carry out certain tests on mobile stations, only the downlink pilot may be active, however. R&S WinIQSIM[™] considers this fact by generating the downlink signals only.

With cdmaOne, the previous US standard for CDMA technology is included in R&S WinIQSIM[™]. With cdma2000, the following generation of the US standard has also been implemented. R&S WinIQSIM[™] supports the modes 1X with 1.2288 Mcps and 3X with 3.6864 Mcps; the 3X mode can optionally be generated according to the directspread or multicarrier method. Up to four mobile or base stations can be simulated simultaneously. The same applies to the 1xEV-DO standard (see Fig. 11a, page 6), which represents a further development of the cdma2000 1x mode and is also supported by R&S WinIQSIM[™]. 1xEV-DO stands for cdma2000 1x Evolution Data Only. This standard enables packet-oriented data transfer at a rate of up to 2.4 Mbps in a 1.25 MHz cdma2000 1x channel.

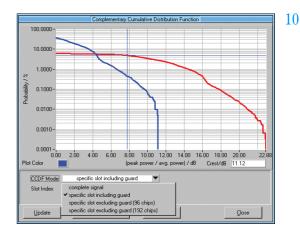


User-defined settings of the timeslots for 3GPP TDD mode



Configuration of TD-SCDMA signal

CCDF of a TDD signal calculated for the complete signal (red) and for an active timeslot (blue)



The open software concept of R&S WinIQSIM[™] allows continuous adaptation to the rapid development of thirdgeneration mobile radio standards. The user is thus always up to the state of the art. Due to the superposition of many code channels, high power peaks occur in all CDMA and WCDMA signals, which is reflected in a high crest factor. This means that a wide dynamic range is required for the transmission system with all its components such as power amplifiers. Since extreme signal peaks are relatively rare, as can be seen from the CCDF, clipping of the signal peaks can be performed without essentially degrading the bit error rate. Clipping prior to baseband filtering does not cause a change in the frequency spectrum of the signal, either.

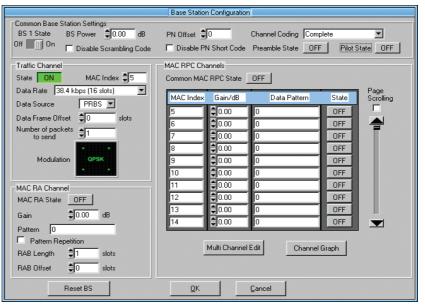
The clipping level can be set between 1% and 100% relative to the maximum level peak. In the TDD mode of the 3GPP standard, and also with TD-SCDMA, scalar clipping is available in addition to conventional vector clipping.

W-LAN (13)

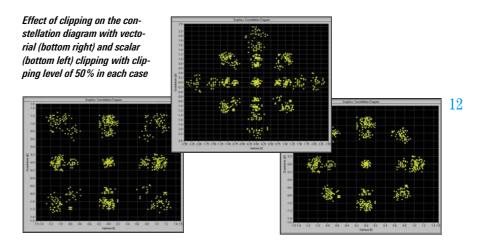
In addition to the comprehensive functionality for the mobile radio standards, R&S WinIQSIM[™] also covers the Wireless LAN standards IEEE 802.11a, IEEE 802.11b and IEEE 802.11g.

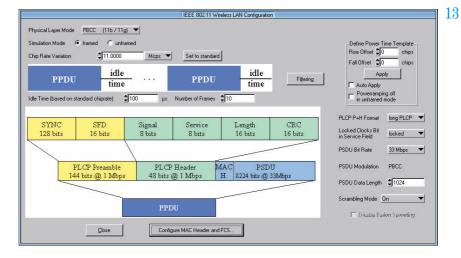
The OFDM modulation mode of 802.11a and 802.11g is supported by R&S Win-IQSIM[™], including all bit rates from 6 to 54 Mbps with full channel coding.

R&S WinIQSIM[™] is also capable of generating signals to IEEE 802.11b. It supports the four data rates 1 Mbps, 2 Mbps, 5.5 Mbps and 11 Mbps as well as all the possible modulation modes DBPSK, DQPSK and CCK. A direct sequence spread spectrum method is used for radio transmission. Irrespective of the data rate, a chip rate of 11 Mcps is used with this method.



Configuration of a 1xEV-DO base station





Operating menu for Wireless LAN standard IEEE 802.11 (a,b,g)

11

Additionally the 802.11b and g modes include PBCC with 5.5, 11, 22 and 33 Mbps.

Since data is transferred in packets with IEEE 802.11a, b and g, R&S WinIQSIM[™] enables the number of packets, the packet length and the idle time between the packets to be entered. For test purposes, R&S WinIQSIM[™] can additionally simulate a continuous data stream without packet structure (unframed mode).

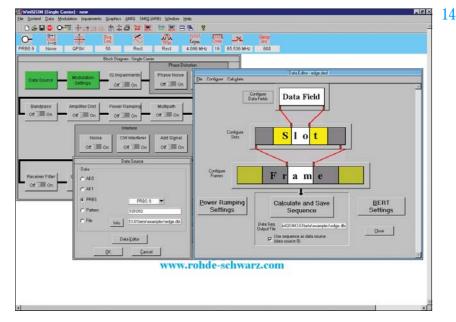
Other OFDM standards (e.g. HIPERLAN/ 2) are covered by the additional software program R&S WinIQOFDM*).

*) Available at www.rohde-schwarz.com

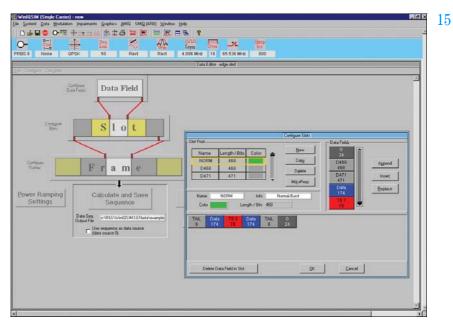
Data editor (14, 15)

Another special feature of R&S WinIQSIM[™] is the data editor for convenient generation of TDMA frame structures, which is especially designed for the single-carrier mode. R&S WinIQSIM[™] already provides preconfigured files for the main TDMA standards such as GSM, GSM/EDGE, DECT, PDC and NADC. A choice of different burst types with the associated data structure is available for the individual systems. Frame and timeslot configuration conform to the relevant standard. Basic configurations can easily be modified, stored and used again in subsequent tests.

The data editor provides users involved in defining or developing new TDMA standards with an almost infinite number of possibilities. The structure of a TDMA signal with its basic elements (data fields of a burst) can be completely defined and successively configured into bursts and frames. In this way, it is possible to design an individual standard. In addition to the graphical representation of the data structures, power ramping can also be defined at the data level.



Main menu of data editor

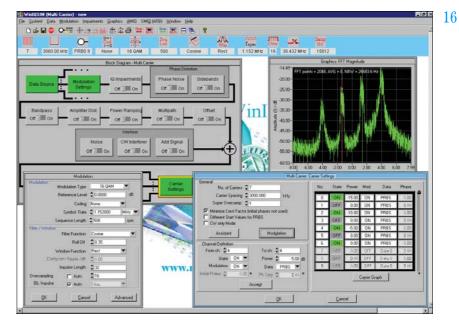


Definition of slots in the data editor

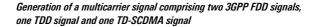
Multicarrier signals (16, 17)

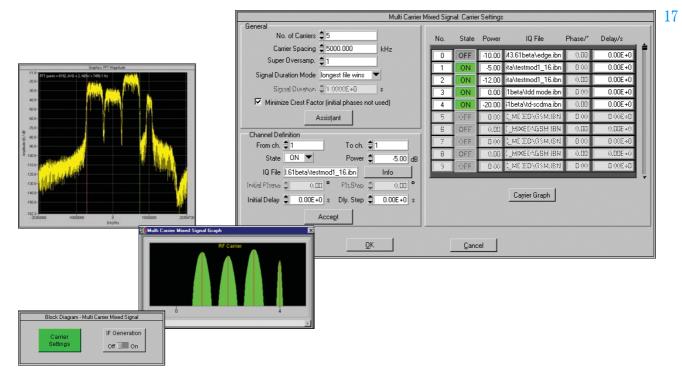
In addition to single-carrier signals, multicarrier signals with all their characteristic parameters such as number of carriers (up to 512), carrier spacing, modulation (same for all carriers) and carrier power can be simulated. In this way, composite signals consisting of modulated and unmodulated carriers or signals with several superimposed impairments can be generated. What makes this application so attractive is that only one generator is needed to produce these signals, which means an enormous cost benefit.

Another operating mode (multicarrier mixed signal mode) allows up to 32 differently modulated carriers to be combined with any signal from various systems (single-carrier, multicarrier, 3GPP FDD and TDD, TD-SCDMA, cdma2000, IS-95) at variable power levels. Signal scenarios such as several different WCDMA carriers can thus be simultaneously simulated and generated by the R&S SMU-B10, R&S SMIQB60 or R&S AMIQ.



Generation of a multicarrier signal





Import system (18)

Data from other PC programs can be read in via the import system. The TCP/IP or the dynamic data exchange (DDE) interface serves as the software interface.

Data can, for example, be imported from the R&S WinIQOFDM software, which is used for generating OFDM-modulated signals. Through subsequent processing in R&S WinIQSIM[™], signal modifications such as baseband filtering and superimposed impairments can be applied to the signal to be generated.

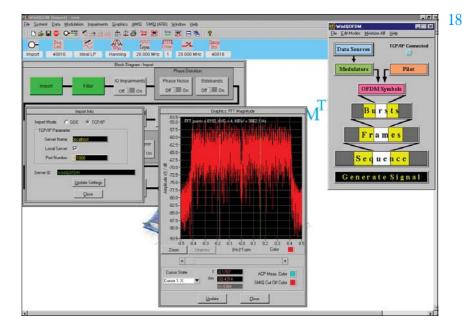
The import interface also forms the basis for further applications (e.g. R&S IQWizard, for more information see www.rohde-schwarz.com) or customerspecific enhancements.

Remote-control functions (19)

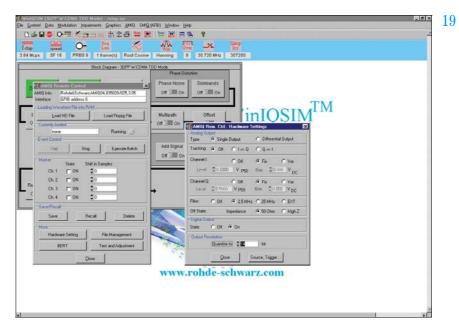
The R&S WinIQSIM[™] PC program is used to control and operate the internal arbitrary waveform generators of R&S SMU (R&S SMU-B10), R&S SMIQ (R&S SMIQB60) and the I/Q Modulation Generators R&S AMIQ.

For the R&S AMIQ, it provides file management on the internal hard disk, and controls the hardware settings and all other functions.

The functionality of R&S WinIQSIM[™] regarding device control is especially important in bit error rate measurements with the R&S AMIQ (option R&S AMIQ-B1). In addition to performing the control functions, the software outputs the measurement results in an R&S WinIQSIM[™] window.



Functioning of the import system with R&S WinIQOFDM software



User interface for controlling the R&S AMIQ with R&S WinIQSIM™

Specifications

User interface	Windows interface with context-sensitive help
Systems	single-carrier, multicarrier, multicarrier mixed signal, 3GPP FDD, 3GPP TDD, TD-SCDMA, IS-95, cdma2000

Single carrier

Simulation of digitally modulated single	e-carrier signals incl. TDMA
Modulation modes PSK Parameter OAM Parameter FSK Parameter User-specific modulation	BPSK, QPSK, offset QPSK, π /4DQPSK, 8PSK, 8PSK EDGE; reference level = -10 dB to 3 dB PSK rotation = 0 to 15 × π /8 16/32/64/256QAM reference level = -10 dB to 3 dB MSK, 2FSK, 4FSK, GTFM modulation index = 0.1 to 12 GTFM b = 0 to 1 definition of customized modulation modes (PSK, QAM, FSK) via data inter- face with up to 4.096 mapping states
Baseband filters	Fourier approximation design method with windowing
Digital filters	rectangular $\sqrt{\text{cosine}}, \alpha = 0.01 \text{ to } 0.99$ $\text{cosine}, \alpha = 0.01 \text{ to } 0.99$ $\text{Gaussian}, B \times T = 0.1 \text{ to } 3.0$ Gaussian EDGE partial response no filter
User-specific filter	customized filter defined via file inter- face, specification of impulse response in time domain with up to 1024 coeffi- cients, different filter coefficients for I and Q channel possible
Window	rectangular Hanning Kaiser, $\beta = 0.01$ to 10.0 Hamming Chebyshev, ripple = 10 dB to 80 dB
Window length	1 to 32 (integer)
Oversampling	1 to 32 (integer)
Symbol rate	10 symb/s to max. 100 Msymb/s
Coding	Gray, Diff, Gray Diff, GSM Diff, NADC, TFTS, MSAT Diff, Phase Diff, none
Data sources	all 0, all 1, PRBS (7, 9, 11, 15, 16, 20, 21, 23), pattern (max. 79 bit), user-defined data sequence via file interface
Data editor	definition of TDMA data structures with modularity at three levels: data field, slot and frame, definition of power-time templates
Data fields	up to 50 different fields, length up to 1000 bit, data content: all 0, all 1, pat- tern (max. 79 bit) or PRBS
Slots	up to 24 different slots, any combination of up to 36 data fields
Frame	any combination of up to 36 slots
Sequence length	1 to max. 4 (16) Msample (R&S AMIQ03/R&S AMIQ04)

Simulation of impairments and transfer characteristics		
I/Q impairment	carrier leakage I and Q (-50% to +50%) I/Q imbalance $(-30\% \text{ to } +30\%)$ quadrature offset $(-30^{\circ} \text{ to } +30^{\circ})$ AM/AM conversion (k2; k3 -3 to +3 dB) AM/ ϕ M conversion (k2; k3 -30° to +30°)	
Phase noise	simulation of impairments of phase- locked loop (VCO) and discrete spurious lines	
Bandpass	simulation of bandpass at the RF with amplitude and group delay distortion	
Amplifier models	amplifiers with soft and hard limiting, nonlinearities: AM/AM k3, k5 –3 dB to +3 dB; AM/φM k3, k5 –30° to +30°	
Power ramping	ramp function: linear, cos ² rise/fall time: 0 to 16 Tsymb level: –80 dB to 0 dB	
Multipath propagation	up to 6 paths with different delays, start phases and levels	
Offset	phase offset: -180° to $+180^{\circ}$ frequency offset: $-0.35 f_{sample}$ to $+0.35 f_{sample}$	
Additive impairments		
Noise	$E_{b}/N_{0}=-3$ dB to +80 dB, bandwidth 0.5/1/2/4/8/16 $f_{\mbox{symbol}}$	
Sinewave interferer	$C/I=-3~dB$ to +80 dB, frequency $-0.35~f_{\text{sample}}$ to +0.35 f_{sample}	
Superimposed signal	addition of a previously calculated signal, level —80 dB to +3 dB	
Receiver filters	rectangular $\sqrt{\text{cosine}}$, $\alpha = 0.01$ to 0.99 Gaussian, B \times T = 0.1 to 3.0 user-specific (see above)	
Quantization	I/Q resolution:1 \times 10 ⁻⁶ to 0.5; filter coefficient resolution: 10 ⁻⁶ to 0.5	
Smoothing	smoothing the wraparound of the I/Q signal between signal end and signal start: in range 2 sample to 32 sample	
Graphical output	user-selectable scaling, zoom function, delta marker; display modes: i(t), q(t), r(t), phi(t), r(t), f(t), eye I, eye Q, eye F, vector diagram, constellation diagram, magnitude/phase/group-delay spec- trum, additionally CCDF and ACP (see below)	
CCDF function	determination and graphical display of complementary cumulative distribution function with calculation of crest factor	
ACP calculation	calculation of adjacent-channel power in the spectrum display (ACP up, low and ACP up 1st alt, low 1st alt)	
IF signal generation	modulation of calculated I/Q signal to IF in range 0.01 MHz to 25 MHz (output to I channel of R&S AMIQ)	

Multicarrier

Simulation of multicarrier signals with same or without modulation

-	
Number of carriers	max. 512 carriers
Parameters of each carrier	state on/off, power, modulation on/off, data source, start phase
Modulation modes	same as with single-carrier system, each carrier can be modulated or not, modulated carriers use the same mod- ulation mode
Baseband filtering	same as with single-carrier system, identical for all modulated carriers
Coding	same as with single-carrier system, identical for all modulated carriers
Data sources	4 different sources, 3 same as in single- carrier system, another PRBS source with different start values for different carriers
Data editor	same as with single-carrier system
Carrier power	-80 dB to 0 dB
Start phase of carrier CW signal	0° to 360° selectable for each carrier or automatic setting for minimizing the crest factor
Sequence length	R&S SMU-B10: 1 to 56 Msample R&S SMIQB60: 1 to 524216 sample R&S AMIQO4: 1 to max. 16 Msample
Simulation of impairments and transfer characteristics	same as with single-carrier system, identical for all modulated carriers
Smoothing	same as with single-carrier system
Graphical output	same as with single-carrier system
CDF function	same as with single-carrier system
ACP calculation	same as with single-carrier system
IF signal generation	same as with single-carrier system

Multicarrier mixed signal

Simulation of differently modulated multicarrier signals and signals of different systems on the carriers

Number of carriers	max. 32
Parameters of each carrier	state on/off, power, I/Q modulation file, start phase
I/Q modulation signal	an I/Q signal file onto which the carrier is to be modulated can be defined for each carrier; these signal files can be generated in all systems
Carrier power	-80 dB to 0 dB
Start phase of carrier CW signal	0° to 360° selectable for each carrier or automatic setting for minimizing the crest factor
Signal period	automatically adapted to longest or shortest carrier signal period or user- selectable (max. duration 4 Msample (16 Msample)/sample rate, R&S AMIQ03/R&S AMIQ04)
Graphical output	same as with single-carrier system
CCDF function	same as with single-carrier system
ACP calculation	same as with single-carrier system
IF signal generation	same as with single-carrier system

3GPP FDD

TS25.101 and TS25.104 General settings	
Chip rate	
Standard Range	3.840 Mcps (15 slots/frame) 10 cps to 100 Mcps
Link direction	uplink (reverse link) and downlink (for- ward link)
Sequence length	R&S SMU-B10: 1 to 380 frames (oversampling 4) R&S SMIQB60: 1 to 6 frames (oversampling 2) R&S AMIQ04: 1 to 104 frames (oversampling 4)
Baseband filter Standard Other filters	$\sqrt{\cos}$, $\alpha = 0.22$ same as with single-carrier system
Clipping level	setting of clipping value relative to highest peak in percent; clipping takes place prior to baseband filtering and reduces the crest factor; range 1% to 100%
Code channels Downlink Uplink	up to 512 data channels (plus special channels) divided among up to four base stations (BS) with 128 code chan nels each up to four mobile stations (MS) each operating in one of modes PRACH only, PCPCH only, DPCCH + DPDCHs
Physical channels in downlink	
P-CPICH Symbol rate Channelization code Slot structure	Primary Common Pilot Channel 15 ksps, fixed 0, fixed predefined symbols
S-CPICH Symbol rate Channelization code Slot structure	Secondary Common Pilot Channel 15 ksps, fixed 0 to 255 predefined symbols
P-SCH Symbol rate Slot structure	Primary Sync Channel 15 ksps, fixed synchronization code (SC)
S-SCH Symbol rate Slot structure	Secondary Sync Channel 15 ksps, fixed synchronization code (SC)
P-CCPCH Symbol rate Channelization code Slot structure	Primary Common Control Physical Channel 15 ksps, fixed 1, fixed data
S-CCPCH Symbol rate Channelization code Slot structure	Secondary Common Control Physical Channel 15, 30, 60, 120, 240, 480, 960 ksps depending on symbol rate, 0 to max. 255 data, TFCI, pilot
PICH Symbol rate Channelization code Number of PIs per frame Slot structure	Page Indication Channel 15 ksps, fixed 0 to 255 18, 36, 72, 144 page indicator bits, not used bits

ccess Preamble Acquisition Indication hannel 5 ksps, fixed to 255
5 ksps, fixed
to 255
equisition indicators, empty symbols
cquisition Indication Channel
5 ksps, fixed
to 255
equisition indicators, empty symbols
hysical Downlink Shared Channel
5, 30, 60, 120, 240, 480, 960 ksps epending on symbol rate,
to max. 255
ata
edicated Physical Control Channel
5 ksps, fixed
to 511
PC, pilot
edicated Physical Channel
5, 15, 30, 60, 120, 240, 480, 960 ksps
epending on symbol rate
to max. 511 ata 1, TPC, TFCI, data 2, pilot
igh Speed Shared Control Channel
) ksps, fixed to 127
ata
igh Speed Physical Downlink Shared
hannel
40 ksps, fixed
to 15
ata
igh Speed Physical Downlink Shared
hannel 10 kops, fixed
40 ksps, fixed to 15
ata
hysical Random Access Channel
5, 30, 60, 120 ksps
reamble(s), message part consisting
data and control section
60 dB to 0 dB
60 dB to 0 dB 60 dB to 0 dB
to 10
to 15
to 14
or 2 frames
to 1023 RBS: PN9, PN11, PN15, PN16
ABA PINE PINE PINE PINE PINE
I 0, all 1 and bit pattern (max. length

PCPCH Symbol ratePhysical Common Packet ChannelFrame structure15, 30, 60, 120 ksps access preamble(), collision detection preamble, power control preamble, nessage part consisting of data and control sectionPreamble part power -60 dB to 0 dB-60 dB to 0 dBPreamble power step Data part power -60 dB to 10 dB-60 dB to 10 dBPreamble power step Data part power Preamble power control preamble length FBI state10 10 frames O r 14POCCH Super control preamble length FBI state0 r 61 slot 30 dBDPCCH Super control preamble length FBI state0 r 61 slot 30 dBDPCCH Super control offset Channelization code DL-UL timing offset FBI pattern channelization code DL-UL timing offset0 r 8 slots OFF/1 bit/2 bit FBI pattern (max. length 16 bit) 10 24 chips, fixedFBI pattern FBI pattern Channelization code DL-UL timing offset FDC of dynamic output Power control step0 r 8 slots OFF/1 bit/2 bit 16 bit)FCD at a Power control step0 r 60 all 1 and bit pattern (max. length 16 bit)TFCI state Power control step0 r 1023 10 all 1 and bit pattern (max. length 16 bit)DPCCH Power control stepDedicated Physical Data Channel verail data rate of all uplink DPDCHs 10 all 0, all 1 and bit pattern (max. length 16 bit)TFCI state TFCI of dynamic output Power control step1 to 63 (depending on overall symbol rateDPDCH Overall symbol rate Channelization code Drock for active DPDCHs, depending on verall symbol rateSymbol rate Channelization code Drock for active DPDCHs, depending on ver		
Frame structureaccess preamble(s), collision detection preamble, power control preamble, ower control sectionPreamble part power-60 dB to 0 dBData part power-60 dB to 0 dBControl part power-60 dB to 0 dBPreamble power step0 dB to 10 dBShared resource modeON/OFFPreamble part length1 to 10Signature0 to 15Access slot0 r 4Message part length1 to 10 framesPower control preamble length0 r 4 slotsFBI patternall 0, all 1 and bit pattern (max. length 16 bit)User dataDeficated Physical Control Channel 15 ksps, fixedOFCHDedicated Physical Control Channel 16 bit)Symbol rateO, fixedD-U-U timing offset1024 chips, fixedFBI patternall 0, all 1 and bit pattern (max. length 16 bit)DPCCHDedicated Physical Control Channel 15 ksps, fixedSymbol rateO, fixedD-U-U timing offsetDedicated Physical Control Channel 1024 chips, fixedChannelization codeO, fixedD-U-U timing offsetDedicated Physical Data Channel over controlVower control step-10 dB to +10 dBDPDCHDedicated Physical Data Channel overall dst nate of all uplink DPDCHsOutput power control step104 for active DPDCHs, depending on overall symbol rateOutput power control step10 dB for all DPDCHsDPDCHDedicated Physical Data Channel overall symbol rateChannelization codeVowerOwerall sym		
Symbol rate15 ksps, fixedChannelization code0, fixedDL-UL timing offset1024 chips, fixedFBI state0FF/1 bit/2 bitFBI state0/F/1 bit/2 bitFBI state0/VOFFTFCI state0/VOFFTFCI0 to 1023Use TPC for dynamic output0/VOFFPower controlIf this function is active, the TPC pattern is used to vary the transmit power of the MS code channels versus time. -10 dB to +10 dBDPDCHDedicated Physical Data Channel overall data rate of all uplink DPDCHs 15, 30, 60, 120, 240, 480, 960, 2 × 960, 3 × 960, 4 × 960, 5 × 960, 6 × 960 ksps 1 to 6, depending on overall symbol rateSymbol ratefixed for active DPDCHs, depending on overall symbol rate fixed for active DPDCHs, depending on overall symbol rate Channelization codeChannel power User data0N/OFFState0N/OFF2nd search code group0 to 63 (depending on scrambling code)State0N/OFFTFCI0 to 1023TFCI state0N/OFFTFCI0 to 1023TFCI0 to 1023TFCI state0N/OFFTFCI0 to 1023TFCI state0N/OFFTFCI0 to 1023TFCI state0N/OFFTFCI0 to 1023TFC pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10Use TPC for dynamic output power control0/VOFFIf this function is active, the TPC pat- tern is used to vary the tr	Frame structure Preamble part power Data part power Control part power Preamble power step Shared resource mode Preamble repetition Signature Access slot Message part length Power control preamble length FBI state FBI pattern	access preamble(s), collision detection preamble, power control preamble, message part consisting of data and control section -60 dB to 0 dB -60 dB to 0 dB -60 dB to 0 dB 0 dB to 10 dB 0 N/OFF 1 to 10 0 to 15 0 to 14 1 to 10 frames 0 or 8 slots 0 FF/1 bit/2 bit all 0, all 1 and bit pattern (max. length 16 bit) PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern
Channelization code DL-UL timing offset0, fixedDL-UL timing offset1024 chips, fixedFBI stateOFF/1 bit/2 bitFBI patternall 0, all 1 and bit pattern (max. length 16 bit)TFCI stateON/OFFTFCIU to 1023Use TPC for dynamic outputON/OFFPower controlIf this function is active, the TPC pat- tern is used to vary the transmit power of the MS code channels versus time. -10 dB to +10 dBDPDCH Overall symbol rateDedicated Physical Data Channel overall data rate of all uplink DPDCHs 15, 30, 60, 120, 240, 480, 960, 2 × 960, 3 × 960, 4 × 960, 5 × 960, 6 × 960 ksps 1 to 6, depending on overall symbol rateSymbol ratefixed for active DPDCHs, depending on overall symbol rate fox dataChannelization codefixed for active DPDCHs, depending on overall symbol rate fox dataSymbol ratefixed for active DPDCHs, depending on overall symbol rate fox dataChannel power User data0 to 63 (depending on scrambling code)StateON/OFF2nd search code group0 to 63 (depending on scrambling code)StateON/OFFTFCI0 to 1023TPC pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01 single + alternating 01 single + alternating 01Use TPC for dynamic output power controlON/OFFIFCIIf this function is active, the TPC pat- tern is used to vary the transmit power of the code channels versus time.	DPCCH	Dedicated Physical Control Channel
Overall symbol rateoverall data rate of all uplink DPDCHs15, 30, 60, 120, 240, 480, 960, 2 × 960, 3 × 960, 4 × 960, 5 × 960, 6 × 960 kspsActive DPDCHsfixed for active DPDCHs, depending on overall symbol rateSymbol ratefixed for active DPDCHs, depending on overall symbol rateChannelization codefixed for active DPDCHs, depending on overall symbol rate -60 dB to 0 dB for all DPDCHsChannel power User data-60 dB to 0 dB for all DPDCHs PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length 16 bit)Parameters for each base station (BS)StateON/OFF2nd search code group0 to 63 (depending on scrambling 	Channelization code DL-UL timing offset FBI state FBI pattern TFCI state TFCI Use TPC for dynamic output Power control	15 ksps, fixed 0, fixed 1024 chips, fixed 0FF/1 bit/2 bit all 0, all 1 and bit pattern (max. length 16 bit) 0N/0FF 0 to 1023 0N/0FF If this function is active, the TPC pat- tern is used to vary the transmit power of the MS code channels versus time.
Overall symbol rateoverall data rate of all uplink DPDCHs15, 30, 60, 120, 240, 480, 960, 2 × 960, 3 × 960, 4 × 960, 5 × 960, 6 × 960 kspsActive DPDCHsfixed for active DPDCHs, depending on overall symbol rateSymbol ratefixed for active DPDCHs, depending on overall symbol rateChannelization codefixed for active DPDCHs, depending on overall symbol rate -60 dB to 0 dB for all DPDCHsChannel power User data-60 dB to 0 dB for all DPDCHs PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length 16 bit)Parameters for each base station (BS)StateON/OFF2nd search code group0 to 63 (depending on scrambling code)Scrambling code0 to 5FFFF hex or offTFCI0 to 1023TPC pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 01Use TPC for dynamic output power controlON/OFFIf this function is active, the TPC pat- tern is used to vary the transmit power of the code channels versus time.	ПРПСН	Dedicated Physical Data Channel
Channelization codeoverall symbol rateChannel power-60 dB to active DPDCHs, depending on overall symbol rate-60 dB to 0 dB for all DPDCHs PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length 16 bit)Parameters for each base station (BS)StateON/OFF2nd search code group0 to 63 (depending on scrambling code)Strambling code0 to 5FFFF hex or offTFCI stateON/OFFTFCI0 to 1023TPC pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, 	Overall symbol rate	overall data rate of all uplink DPDCHs 15, 30, 60, 120, 240, 480, 960, 2×960 , 3×960 , 4×960 , 5×960 , 6×960 ksps 1 to 6, depending on overall symbol
Channelization code Channel power User datafixed for active DPDCHs, depending on overall symbol rate -60 dB to 0 dB for all DPDCHs PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length 16 bit)Parameters for each base station (BS)StateON/OFF2nd search code group0 to 63 (depending on scrambling code)Strambling code0 to 5FFFF hex or offTFCI stateON/OFFTFCI pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, 	Symbol rate	
Channel power User data-60 dB to 0 dB for all DPDCHs PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length 16 bit)Parameters for each base station (BS)StateON/OFF2nd search code group0 to 63 (depending on scrambling code)Strambling code0 to 5FFFF hex or offTFCI state0N/OFFTFCI0 to 1023TPC pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10Use TPC for dynamic output power controlON/OFFft this function is active, the TPC pat- tern is used to vary the transmit power of the code channels versus time.	Channelization code	fixed for active DPDCHs, depending on
StateON/OFF2nd search code group0 to 63 (depending on scrambling code)Scrambling code0 to 5FFFF hex or offTFCI stateON/OFFTFCI0 to 1023TPC pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10Use TPC for dynamic output power controlON/OFF If this function is active, the TPC pat- tern is used to vary the transmit power of the code channels versus time.		-60 dB to 0 dB for all DPDCHs PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length
2nd search code group0 to 63 (depending on scrambling code)Scrambling code0 to 5FFFF hex or offTFCI state0N/0FFTFCI0 to 1023TPC pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10Use TPC for dynamic output power control0N/0FFIf this function is active, the TPC pat- tern is used to vary the transmit power 	Parameters for each base station (BS)
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TFCI stateON/OFFTFCI0 to 1023TPC pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10Use TPC for dynamic output power controlON/OFF If this function is active, the TPC pat- tern is used to vary the transmit power of the code channels versus time.	2nd search code group	
TFCI0 to 1023TPC pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10Use TPC for dynamic output power controlON/OFF If this function is active, the TPC pat- tern is used to vary the transmit power of the code channels versus time.	Scrambling code	0 to 5FFFF hex or off
TPC pattern readout modeuse of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10Use TPC for dynamic output power controlON/OFF If this function is active, the TPC pat- tern is used to vary the transmit power of the code channels versus time.	TFCI state	ON/OFF
continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10Use TPC for dynamic output power controlON/OFF If this function is active, the TPC pat- tern is used to vary the transmit power of the code channels versus time.	TFCI	0 to 1023
control If this function is active, the TPC pat- tern is used to vary the transmit power of the code channels versus time.	TPC pattern readout mode	continuous, single + all 0, single + all 1, single + alternating 01,
Output power control step -10 dB to +10 dB		If this function is active, the TPC pat- tern is used to vary the transmit power
	Output power control step	-10 dB to +10 dB

Transmit diversity	OFF/antenna 1/antenna 2 If this function is active, the output sig- nal for antenna 1 or antenna 2 can be generated as defined in the standard.	
Parameters for each mobile station (N	MS)	
State	0N/0FF	
Mode	PRACH only, PCPCH only, DPCCH + DPDCHs	
Scrambling code	0 to FF FFFF hex	
Scrambling code mode	long, short, off	
TPC pattern	all 0, all 1 and bit pattern (max. length 16 bit)	
TPC pattern readout mode	use of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10	
Parameters independently selectable	for each downlink code channel	
State	ON/OFF	
Symbol rate	7.5 ksps to 960 ksps, depending on type of physical channel	
Channelization code	0 to max. 511, depending on symbol rate and type of physical channel	
Power	-60 dB to 0 dB	
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length 16 bit)	
Timing offset	separately adjustable for each code channel 0 to 149 (in units of 256 chips)	
Pilot length	2, 4, 8, 16 bit, depending on symbol rate	
TPC pattern	all 0, all 1 and bit pattern (max. length 16 bit)	
Multicode state	ON/OFF	
Compressed mode		
State	ON/OFF	
Compressed mode method	higher layer scheduling, puncturing (downlink only) or SF/2	
Downlink frame structure	type A (last pilot) or type B (first TPC, last pilot)	
Power offset for compressed slots	automatic or manual in range 0 dB to 10 dB	
Number of transmission patterns	1 or 2	
Number of transmission gaps per pattern	2	
TGSN (transmission gap slot number) TGL1, TGL2 (transmission gap length 1, 2) TGD1, TGD2 (transmission gap distance 1, 2) TGPL1, TGPL2 (transmission gap pattern length 1, 2)	user-selectable within the range per- mitted by the standard; conflicting pa- rameters are displayed and solutions proposed	
Assistant functions to facilitate operation		
Test models (supplied as example files)	test model 1 with 16/32/64 channels test model 2 test model 3 with 16/32 channels test model 4	

Parameterizable predefined settings	Generation of complex signal scenarios in downlink with parameterizable de- fault settings. Selectable parameters: use and symbol rate of special chan- nels (for synchronization of mobile sta- tion), number and symbol rate of data chan- nels, crest factor: minimal/average/ worst
Multichannel edit	Common configuration of data chan- nels of BS channel table. Selectable parameters, partly with start value and step size: range of data channels to be set, symbol rate, channelization code with step size, data, TPC, timing offset with step size, multicode state, state
Copy BS/MS	Adopting the configuration of a BS for another BS/MS to define multi-BS/MS scenarios or BS signals with more than 128 channels. Parameters: source and destination of copying, channelization code offset for simple definition of BS signals with more than 128 channels and continuous channel- ization codes
Resolve domain conflicts	Elimination of code channel overlap- ping in code domain (domain conflicts) occurring in a BS/MS
Graphical displays	
Domain conflicts	Display of domain conflicts (overlap- ping of code channels in code domain) in the channel table lines concerned. The code domain occupied by the code channels involved in the conflict can also be displayed.
Code domain	Display of code domain occupied by current BS. Domain areas in which con- flicts occur are highlighted. The distri- bution of code channels in the code do- main as well as the channel powers are shown qualitatively.
Channel graph	Display of all active channels of a BS versus the channel table index. The powers of the code channels are shown qualitatively.
CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of complex I/O samples exceeding a predefined threshold. To- gether with the current CCDF, the CCDFs of the two 3GPP signals last generated can be displayed to observe the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
Constellation diagram	Display of constellation diagram versus I/Q samples of current 3GPP signal. This diagram allows qualitative assess- ment of channel configuration, channel power ratios, and effect of parameters such as data and data offset.

3GPP TDD (with option R&S SMU-K13/R&S SMIQK13/R&S AMIQK13)

Simulation of signals to time division duplex wideband CDMA standard according to version 4.1.0 of the 3GPP Technical Specification TS 25.221, TS 25.223, available as Software Option R&S SMU-K13 and internal Arbitrary Waveform Generator of Option R&S SMU-B10 of the R&S SMU or Software Option R&S SMIQK13 and Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ or Software Option R&S AMIQK13 of the R&S AMIQ

General settings		
Chip rate Standard Range	3.84 Mcps see clock rates of R&S SMU-B10/ R&S SMIQB60/R&S AMIQ in the corre- sponding datasheets	
Mode	downlink only: the base station compo- nents of a cell are active uplink only: the mobile station compo- nents of a cell are active downlink and uplink: both the base sta- tion and the mobile station compo- nents of a cell are active	
Sequence length	entry in slots (0.667 ms each) or frames (10 ms each), max. length depending on oversampling R&S SMU-B10 (for oversampling 4): 380 frames R&S SMIQB60 (for oversampling 2 in R&S WinIQSIM™ (oversampling >4 as a result of hardware oversampling in the R&S SMIQ): 1 to 6 frames R&S AMIQ04 (for oversampling 4): 1 to 104 frames	
Baseband filter Standard Other filters	root raised cosine filter, roll off = 0.22 same as with single-carrier system	
Cells	simulation of up to 4 cells, each com- prising 15 slots	
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering. Either scalar mode or vector mode can be se- lected. Clipping reduces the crest fac- tor. The range is 1% to 100%.	
Parameters for each cell		
State	ON/OFF	
Frame structure	The link direction (uplink or downlink) can be set independently for each of the 15 slots of the frame. All single- and multi-switching point configurations can be simulated.	
Scrambling code	0 to 127 scrambling code can be disabled for testing	
Code group	automatic selection depending on scrambling code 0 to 31	
Midamble allocation method	default common equal to scrambling code	
Guard field power state	OFF (according to standard)/ON	
Parameters for each downlink slot		
State	ON/OFF	
Slot mode	downlink dedicated: simulation of up to 16 DPCHs and max. 6 special channels	

Burst type	1 and 2
SCH assoc. t_offset	automatic selection depending on
	scrambling code 0 to 31
SCH code allocation	cases 1 and 2 to TS 25.223
Page indicator length	2, 4, 8
Parameters for each uplink slot	2, 4, 0
Slot mode	uplink dedicated: simulation of up to 16
	DPCHs and 1 special channel PRACH: simulation of one Physical Ran- dom Access Channel
TPC pattern readout mode	application mode for TPC pattern continuous, single + all 0, single + all 1, single + alt. 01, single + alt. 10
Burst type	1, 2 and 3
Parameters in uplink PRACH mode	
Burst type	3
Start frame	selection of first frame in which PRACH is sent 0 to 10
PRACH length	length of PRACH message part 1 to 10 frames
User	index of user to which PRACH is assigned
Midamble and midamble shift	display of midamble used and of midamble time shift, depending on midamble allocation method and user
Spreading factor	8 and 16
Spreading code	spreading code of channel, range de- pending on spreading factor 1 to max. 16
Power	-60 dB to 0 dB
Physical channels	
Data	4 different data sources, 3 same as with single-carrier system, another PBRS source with differing start values for different code channels
Downlink	Primary Common Control Physical Channel (P-CCPCH) Secondary Common Control Physical Channel (S-CCPCH) Primary Sync Channel (P-SCH) Secondary Sync Channel (S-SCH) Physical Downlink Shared Channel (PDSCH) Page Indicator Channel (PICH) Dedicated Physical Channel (DPCH)
Uplink	Physical Random Access Channel (PRACH) Physical Uplink Shared Channel (PUSCH) Dedicated Physical Channel (DPCH)
Parameters independently selectable	
State	ON/OFF
User	1 to 16 with burst types 1 and 3 1 to 6 with burst type 2
Spreading factor	depending on channel type and link direction 1, 2, 4, 8, 16
Spreading code	depending on channel type and spreading factor 1 to max. 16

Midamble and midamble shift	dieplay of midamble used and of
wildamble and midamble shift	display of midamble used and of midamble time shift, depending on midamble allocation method and user
TFCI/TPC combination	combination of TFCI and TPC fields, TPC in uplink only, uplink: TFCI 0 TPC 0, TFCI 0 TPC 2, TFCI 4 TPC 2, TFCI 8 TPC 2, TFCI 16 TPC 2, TFCI 32 TPC 2 downlink: TFCI 0, TFCI 4, TFCI 8, TFCI 16, TFCI 32
TFCI	transport format combination indicator 0 to 1023
TPC pattern	bit pattern (max. length 16 bit) as a data source for the TPC field of the channel, in uplink only
Power	-60 dB to 0 dB
Data	4 different data sources, 3 same as single-carrier system, another PBRS source with differing start values for different code channels
Assistant functions to facilitate opera	ition
Copy cell	Adopting the configuration of a cell for another cell to define multicell scenarios. Parameters: source and destination of copying
Resolve domain conflicts	Elimination of code channel overlap- ping in code domain occurring in a slot (domain conflicts).
Graphical displays	
Domain conflicts	Display of domain conflicts (overlap- ping of code channels in code domain) in the channel table lines concerned. The code domain occupied by the code channels involved in the conflict can also be displayed.
Code domain	Display of code domain occupied by ac- tive slot. Domain areas in which con- flicts occur are highlighted. Code chan- nel distribution in the code domain and channel powers are displayed.
Channel graph	Display of all active channels of a slot versus the channel table index. The powers of the individual code channels are indicated.
CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of the complex I/Q samples exceeding a predefined threshold. Together with the current CCDF, the CCDFs of any number of 3GPP TDD signals generated last can be displayed to observe the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
Constellation diagram	Display of constellation diagram versus I/Q samples of current 3GPP TDD sig- nal. This diagram allows qualitative as- sessment of channel configuration, channel power ratios, and effect of TDD system parameters.

Digital standard TD-SCDMA (with option R&S SMU-K14/R&S SMIQK14/R&S AMIQK14)

Simulation of signals according to time division synchronous CDMA standard of China Wireless Telecommunication Standard Group (CWTS), available as Software Option R&S SMU-K14 and internal Arbitrary Waveform Generator of Option R&S SMU-B10 of the R&S SMU or Software Option R&S SMIQK14 and Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ or Software Option R&S AMIQK14 of the R&S AMIQ

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General settings		
Chip rate Standard Range	1.28 Mcps see clock rates of R&S SMU-B10/ R&S SMIQB60/R&S AMIQ in the corre- sponding datasheets	
Mode	downlink only: the base station compo- nents of a cell are active uplink only: the mobile station compo- nents of a cell are active downlink and uplink: both the base sta- tion and the mobile station compo- nents of a cell are active	
Sequence length	entry in frames (5 ms each), max. length depending on oversampling R&S SMU-B10 (for oversampling 4): 2290 frames R&S SMIQ (B60): (for oversampling 2 in R&S WinIQSIM [™] (oversampling >4 as a result of hardware oversampling in the R&S SMIQ): 1 to 40 frames R&S AMIQO4 (for oversampling 4): 1 to 625 frames	
Baseband filter Standard Other filters	root raised cosine filter, roll off = 0.22 same as with single-carrier system	
Cells	simulation of up to 4 cells, each com- prising 7 traffic slots and 3 special slots	
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering. Either scalar mode or vector mode can be se- lected. Clipping reduces the crest fac- tor. The range is 1% to 100%.	
Parameters for each cell		
State	ON/OFF	
Frame structure	total of 7 traffic slots, slot 0 always re- served for downlink, slot 1 to switching point reserved for uplink, other slots re- served for downlink; special slots between slots 0 and 1: Downlink Pilot Slot (DwPTS), Guard Pe- riod (GP) and Uplink Pilot Slot (UpPTS)	
Scrambling code	0 to 127 scrambling code can be disabled for testing	
SYNC code	automatic selection depending on scrambling code 0 to 31	
Switching point	switchover between uplink and down- link slots 1 to 6	
Layer 1 control fields	can be enabled and disabled to simu- late burst types 1 and 2	
DwPTS power	–60 dB to 0 dB	

Parameters for each downlink slot		
State	ON/OFF	
Slot mode	downlink dedicated: simulation of up to 16 DPCHs and max. 5 special channels	
TPC pattern readout mode	application mode for TPC pattern: continuous, single + hold 01, single + hold 10, single + all up, single + all down	
Sync shift repetition mode	number of frames to which sync shift bits are distributed 1 to 500	
Stealing flag	value of the two stealing bits 0 to 3	
Parameters for each uplink slot		
Slot mode	uplink dedicated: simulation of up to 16 DPCHs PRACH: simulation of one Physical Ran- dom Access Channel	
TPC pattern readout mode	application mode for TPC pattern: continuous, single + hold 01, single + hold 10, single + all up, single + all down	
Sync shift repetition mode	number of frames to which sync shift bits are distributed 1 to 500	
Stealing flag	value of the two stealing bits 0 to 3	
Parameters in uplink PRACH mode		
SYNC 1	SYNC 1 code 0 to 7	
UpPTS start frame	selection of first frame in which UpPTS is sent 1 to 6	
UpPTS repetition	number of UpPTS repetitions 1 to 10	
PRACH length	length of PRACH message part 1 to 10 frames	
Gross data rate	17.6 kbps, 35.2 kbps	
Spreading code	depending on gross data rate 0 to max. 15	
Sync shift pattern	bit pattern (max. length 16 bit) as a data source for the sync shift field of the channel	
TPC pattern	bit pattern (max. length 16 bit) as a data source for the TPC field of the channel	
Power	–60 dB to 0 dB	
Data	4 different data sources, 3 same as with single-carrier system, another PRBS source with differing start values for different code channels	
Physical channels		
Downlink	Primary Common Control Physical Channel (P-CCPCH) Secondary Common Control Physical Channel (S-CCPCH) Physical Forward Access Channel (F-FACH) Downlink Pilot Time Slot (DwPTS) Dedicated Physical Channel (DPCH)	

Uplink	Physical Random Access Channel (PRACH) Uplink Pilot Time Slot (UpPTS) Dedicated Physical Channel (DPCH)
Parameters independently selectable	for each code channel
State	ON/OFF
Gross data rate	depending on channel type 17.6 kbps, 35.2 kbps, 70.4 kbps, 140.8 kbps, 281.6 kbps (spreading factors 1, 2, 4, 8, 16)
Spreading code	depending on channel type and gross data rate 0 to max. 15
Midamble shift	time shift of midamble in chips: 0 to 120, step width 8 chips
Power	-60 dB to 0 dB
Data	4 different data sources, 3 same as with single-carrier system, another PRBS source with differing start values for different code channels
Sync shift pattern	bit pattern (max. length 16 bit) as a data source for the sync shift field of the channel
TPC pattern	bit pattern (max. length 16 bit) as a data source for the TPC field of the channel
Assistant functions to facilitate opera	ition
Predefined settings	Generation of complex signal scenarios with parameterizable default settings. Selectable parameters: use of special channels (P-CCPCH), number and gross data rate of data channels, crest factor: minimal/average/worst
Copy cell	Adopting the configuration of a cell for another cell to define multicell scenari- os. Parameters: source and destination of copying
Resolve domain conflicts	Elimination of code channel overlap- ping in code domain occurring in a slot (domain conflicts).
Graphical displays	
Domain conflicts	Display of domain conflicts (overlap- ping of code channels in code domain) in the channel table lines concerned. The code domain occupied by the code channels involved in the conflict can also be displayed.
Code domain	Graphical Display of code domain occu- pied by active slot. Domain areas in which conflicts occur are highlighted. Code channel distribution in the code domain and channel powers are dis- played.
Channel graph	Display of all active channels of a slot versus the channel table index. The powers of the individual code channels are indicated.

CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of the complex I/Q samples exceeding a predefined threshold. Together with the current CCDF, the CCDFs of any number of TD-SCDMA signals generated last can be displayed to observe the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
Constellation diagram	Display of constellation diagram versus I/O samples of current TD-SCDMA sig- nal. This diagram allows qualitative as- sessment of channel configuration, channel power ratios, and effect of TD-SCDMA system parameters.

Digital standard IS-95

(with option R&S SMU-K11/R&S SMIQK11/R&S AMIQK11)

Simulation of CDMA signals to North American standard cdmaOne, available as Software Option R&S SMU-K11 and internal Arbitrary Waveform Generator or Software Option R&S SMIQK11 in conjunction with the Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ or Option R&S SMU-B10 of the R&S SMU or Software Option R&S AMIQK11 of the R&S AMIQ

General settings

U U	
Chip rate Standard Range Link direction	1.2288 Mcps see clock rates of R&S SMU-B10/ R&S SMIQB60/R&S AMIQ in the corre- sponding datasheets forward link and reverse link
Sequence length	entry in symbols (1536 symbols corre- spond to 80 ms frame), max. length de- pending on oversampling R&S SMU-B10 (for oversampling 4): 1 to 145 frames R&S SMIQB60 (for oversampling 2 in R&S WinIQSIM™, and oversampling >4 as a result of hardware oversam- pling in the R&S SMIQ): 1 to 2 frames R&S AMIQ04 (for oversampling 4): 1 to 40 frames
Baseband filter	
Standard Other filters	cdma2000 1X (corresponds to IS-95 filter) same as with single-carrier system
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering and re- duces the crest factor. The range is 1% to 100%.
Parameters for each base station	PN offset 0 to 511
Parameters for each code channel	state on/off, power, data, long code mask
Physical channels in forward link	
Pilot	
Paging	parameters: data, long code mask with PCN and pilot PN field
Sync	parameter: data
Traffic	parameters: data, long code mask with permuted ESN field

parameters: data, long code mask with ACN, PCN, base ID and pilot PN field
parameters: data, long code mask with permuted ESN field
-40 dB to 0 dB
4 different data sources, 3 same as with single-carrier system, another PRBS source with differing start values for different code channels
same as with single-carrier system
Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of complex I/Q samples exceeding a predefined threshold. To- gether with the current CCDF, the CCDFs of any number of IS-95 signals last generated can be displayed to ob- serve the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
Calculation of adjacent-channel power in spectrum display (ACP up, low and ACP up 1st alt, low 1st alt)
Modulation of calculated I/Q signal to intermediate frequency in range 0.01 MHz to 25 MHz (output to 1 channel of R&S AMIQ)

Digital standard cdma2000 (with option R&S SMU-K12/R&S SMIQK12/R&S AMIQK12)

Simulation of CDMA signals to North American standard IS-2000, available as Software Option R&S SMU-K12 and internal Arbitrary Waveform Generator of Option R&S SMU-B10 of the R&S SMU or Software Option R&S SMI0K12 in conjunction with the Arbitrary Waveform Generator Option R&S SMI0B60 of the R&S SMI0 or Software Option R&S AMI0K12 of the R&S AMI0

General settings	
Chip rate Standard Range	1.2288 Mcps (1X), 3.6864 Mcps (3X) see clock rates of R&S SMU-B10/ R&S SMIQB60/R&S AMIQ in the corre- sponding datasheets
Carrier spacing Standard Variable	1.25 MHz R&S AMIQ: 0 to 10 MHz R&S SMIQB60: 0 to 2 MHz
Modes	1X Direct Spread 3X Direct Spread 3X Multi Carrier (forward link only)
Link direction	forward link and reverse link

Sequence length	
Sequence length	entry in frames of 80 ms, max. length depending on chip rate, mode and oversampling R&S SMU-B10 (for oversampling 4): a) 1 to 145 frames at 1.2288 Mcps (1x) b) 1 to 95 frames at 3.6864 Mcps (3x) Multi Carrier c) 1 to 47 frames at 3.6864 Mcps (3x) Direct Spread R&S SMIQB60 (for oversampling 2 in R&S WinIQSIM TM , and oversampling >4 as a result of hardware oversam- pling in the R&S SMIQ): a) 1 to 2 frames at 1.2288 Mcps (1X) b) 1 frame at 3.6864 Mcps (3X) Multi Carrier c)1 frame at 3.6864 Mcps (3X) Direct Spread R&S AMIQ04 (for oversampling 4): a) 1 to 26 frames at 1.2288 Mcps (1X) b) 1 to 26 frames at 3.6864 Mcps (3X) Multi Carrier c) 1 to 13 frames at 3.6864 Mcps (3X) Direct Spread
Baseband filter	
Standard Other filters	cdma2000 1X cdma2000 3X Direct Spread same as with single-carrier system
Code channels Forward link Reverse link	4 base stations with max. 91 code channels each (depending on radio configuration) 4 mobile stations with max. 13 code channels each (depending on radio configuration)
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering and re- duces the crest factor. The range is 1% to 100%.
Parameters for each base station (BS)
State	ON/OFF
	ON/OFF RC 1 to RC 5 RC 6 to RC 9
State Radio configuration Chip rate 1.2288 Mcps (1X)	RC 1 to RC 5
State Radio configuration Chip rate 1.2288 Mcps (1X) Chip rate 3.6864 Mcps (3X)	RC 1 to RC 5 RC 6 to RC 9
State Radio configuration Chip rate 1.2288 Mcps (1X) Chip rate 3.6864 Mcps (3X) PN offset	RC 1 to RC 5 RC 6 to RC 9 O to 511
State Radio configuration Chip rate 1.2288 Mcps (1X) Chip rate 3.6864 Mcps (3X) PN offset Quasi-orthogonal Walsh sets	RC 1 to RC 5 RC 6 to RC 9 0 to 511 set 1 to set 3 All levels of channel coding provided by IS-2000 (e.g. frame quality indicator, convolutional encoder, symbol punc- ture and interleaver) are available. All combinations of frame lengths and data rates are supported. Four modes are available: – off: channel coding off – complete: complete channel coding on – without interleaving: channel coding on, but without interleaver – interleaving only: channel coding off,
State Radio configuration Chip rate 1.2288 Mcps (1X) Chip rate 3.6864 Mcps (3X) PN offset Quasi-orthogonal Walsh sets Channel coding	RC 1 to RC 5 RC 6 to RC 9 0 to 511 set 1 to set 3 All levels of channel coding provided by IS-2000 (e.g. frame quality indicator, convolutional encoder, symbol punc- ture and interleaver) are available. All combinations of frame lengths and data rates are supported. Four modes are available: – off: channel coding off – complete: complete channel coding on – without interleaving: channel coding on, but without interleaver – interleaving only: channel coding off, interleaver active only off / antenna 1 / antenna 2 If this function is active, the output sig- nal for antenna 1 or antenna 2 can be
State Radio configuration Chip rate 1.2288 Mcps (1X) Chip rate 3.6864 Mcps (3X) PN offset Quasi-orthogonal Walsh sets Channel coding Transmit diversity (OTD) Use TPC for dynamic output	RC 1 to RC 5 RC 6 to RC 9 0 to 511 set 1 to set 3 All levels of channel coding provided by IS-2000 (e.g. frame quality indicator, convolutional encoder, symbol punc- ture and interleaver) are available. All combinations of frame lengths and data rates are supported. Four modes are available: - off: channel coding off - complete: complete channel coding on - without interleaving: channel coding on, but without interleaver - interleaving only: channel coding off, interleaver active only off / antenna 1 / antenna 2 If this function is active, the output sig- nal for antenna 1 or antenna 2 can be generated as defined in the standard. ON/OFF If this function is active, the TPC pat- tern is used to vary the transmit power

Parameters for each mobile station (MS)		
State	ON/OFF	
Radio configuration Chip rate 1.2288 Mcps (1X) Chip rate 3.6864 Mcps (3X)	RC 1 to RC 4 RC 5 to RC 6	
Channel coding	All levels of channel coding provided by IS-2000 (e.g. frame quality indicator, convolutional encoder, symbol punc- ture and interleaver) are available. All combinations of frame lengths and data rates are supported. Four modes are available: – off: channel coding off – complete: complete channel coding on – without interleaving: channel coding on, but without interleaver – interleaving only: channel coding off, interleaver active only	
Use TPC for dynamic output power control	ON/OFF If this function is active, the TPC pat- tern (selectable bit pattern, max. length 16 bit) is used to vary the transmit pow- er of the code channels versus time.	
Output power control step	-10 dB to +10 dB	
Channel types		
Forward link	Special channels: – Forward Pilot (F-PICH) – Sync (F-SYNC) – Paging (F-PCH) – Transmit Diversity Pilot (F-TDPICH) – Auxiliary Pilot (F-APICH) – Auxiliary Transmit Diversity Pilot (F-ATDPCH) – Broadcast (F-BCH) – Quick Paging (F-OPCH) – Common Power Control (F-CPCCH) – Common Assignment (F-CACH) – Forward Common Control (F-DCCH) Traffic channels: – Forward Dedicated Control (F-DCCH) – Forward Fundamental (F-FCH) – Forward Supplemental (F-SCH)	
Reverse link	Special channels: – Reverse Pilot (R-PICH) – Access (R-ACH) – Enhanced Access (R-EACH) – Reverse Common Control (R-CCCH) – Reverse Dedicated Control (R-DCCH) Traffic channels: – Reverse Fundamental (R-FCH) – Reverse Supplemental Code (R-SCCH) – Reverse Supplemental 1 (R-S1CH) – Reverse Supplemental 2 (R-S2CH)	
Parameters independently selectable		
State Frame length	ON/OFF depending on channel type and radio configuration: 5 ms, 10 ms, 20 ms, 40 ms, 80 ms	
Data rate	depending on channel type and radio configuration: 1.2 kbps to max. 1036.8 kbps	
Walsh code	depending on channel type and radio configuration: 0 to max. 255	
Quasi-orthogonal code	ON/OFF	
Long code mask	0 to 3FF FFFF FFFF hex	

Power	-60 dB to 0 dB
Data	4 different data sources, 3 same as with single-carrier system, another PRBS source with differing start values for different code channels
TPC pattern	bit pattern (max. length 16 bit)
Parameters independently selectable	for each reverse link code channel
State	ON/OFF
Frame length	depending on channel type and radio configuration: 5 ms, 10 ms, 20 ms, 40 ms, 80 ms
Data rate	depending on channel type and radio configuration: 1.2 kbps to max. 1036.8 kbps
Long code mask	0 to 3FF FFFF FFFF hex
Power	–60 dB to 0 dB
Data	4 different data sources, 3 same as with single-carrier system, another PRBS source with differing start values for different code channels
Assistant functions to facilitate operation	ation
Parameterizable predefined settings (forward link only)	Generation of complex signal scenarios with parameterizable default settings. Selectable parameters: – use of special channels (F-PICH, F-SYNC, number of F-QPCHs) – number, frame length and data rate of data channels – crest factor: minimal/average/worst
Multichannel edit (forward link only)	Common configuration of data chan- nels of BS channel table. Selectable parameters, partly with start value and step size: – range of data channels to be set – frame length – data rate – Walsh code with step width – state of quasi-orthogonal Walsh set – channel power with step size – data – TPC – state
Copy BS/MS	Adopting the configuration of a BS/MS for another BS/MS to define multi-BS/ MS scenarios. Parameters: – source and destination of copying – Walsh code offset (forward link)
Graphical displays	
Domain conflicts (forward link only)	Display of domain conflicts (overlap- ping of code channels in code domain) in the channel table lines concerned. The code domain occupied by the code channels involved in the conflict can also be displayed.
Code Domain (forward link only)	Display of code domain occupied by current BS. Domain areas in which con- flicts occur are highlighted. The distri- bution of code channels in the code do- main as well as the channel powers are displayed.
Channel graph	Display of all active channels of a BS/MS versus the channel table index. The powers of the code channels are shown.

CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of complex I/Q samples exceeding a predefined threshold. To- gether with the current CCDF, the CCDFs of any number of cdma2000 sig- nals last generated can be displayed to observe the effect of parameter chang- es. The crest factor of the signal can be seen in the CCDF.
Constellation diagram	Display of constellation diagram versus I/O samples of current CDMA signal. This diagram allows qualitative assess- ment of channel configuration, channel power ratios, and effect of selected Walsh codes.

Digital standard 1xEV-DO (with option R&S SMU-K17/R&S SMIQK17/R&S AMIQK17)

Simulation of 1xEV-DO signals to North American Standard "cdma2000 High Rate Packet Data Air Interface Specification", available as Software Option R&S SMU-K17 and internal Arbitrary Waveform Generator of Option R&S SMU-B10 of the R&S SMU or Software Option R&S SMIQK17 in conjunction with the Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ or Software Option R&S AMIQK17 of the R&S AMIQ

General settings	
Chip rate Standard Range	1.2288 Mcps see clock rates of R&S SMU-B10/ R&S SMIQB60/R&S AMIQ in the corre- sponding datasheets
Link direction	forward link (simulation of up to 4 base stations) and reverse link (simulation of up to 4 mobile stations)
Sequence length	entry in frames of 26.67 ms, max. length depending on oversampling; R&S SMU-B10 (for oversampling 4): 1 to 430 frames R&S AMIQ04 (for oversampling 4): 1 to 122 frames R&S SMIQB60 (for oversampling 2 in R&S WinIQSIM [™] (oversampling >4 as a result of hardware oversampling in the R&S SMIQ): 1 to 7 frames
Baseband filter Standard Other filters	cdma2000 1X cdma2000 1X + equalizer same as with single-carrier system
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering and re- duces the crest factor. The range is 1% to 100%.
Data sources for traffic channels	4 different data sources, 3 same as with single-carrier system, plus 1 PRBS source with differing start values for different code channels
Parameters for each base station (BS)	
State	ON/OFF
BS power	-80 dB to 0 dB
PN offset	0 to 511

Channel coding	All levels of channel coding provided by 1xEV-D0 (e.g. convolutional encoder, symbol puncture and interleaver) are available. Four modes are available: - off: channel coding off - complete: complete channel coding on - without interleaving: channel coding on, but without interleaver - interleaving only: channel coding off, interleaver active only
Parameters for each mobile station (N	/IS)
State	ON/OFF

Mode Access Mode Traffic Mode Channel coding All levels of channel coding provided by 1xEV-DO (e.g. convolutional encoder, symbol puncture and interleaver) are available. Four modes are available: - off: channel coding off - complete: complete channel coding on - without interleaving: channel coding on, but without interleaver - interleaving only: channel coding off, interleaver active only Long code mask I/Q 0 to 3FF FFFF FFFF hex **Channel types** Forward link Pilot Channel, Preamble **Traffic Channel** MAC Reverse Activity Channel (MAC RA) up to 59 MAC Reverse Power Control Channels (MAC RPCs) Reverse Link Access Mode: Pilot Channel Data Channel Traffic Mode: Data Rate Control Channel (DRC) Acknowledge Channel (ACK) **Pilot Channel** Reverse Rate Indicator Channel (RRI) Traffic Channel Data rates and modulation of 38.4 kbps, 16 slots, QPSK forward traffic channel 76.8 kbps, 8 slots, QPSK 153.6 kbps, 4 slots, QPSK 307.2 kbps, 2 slots, QPSK 307.2 kbps, 4 slots, QPSK 614.4 kbps, 1 slot, QPSK 614.4 kbps, 2 slots, QPSK 921.6 kbps, 2 slots, 8PSK 1228.8 kbps, 1 slot, QPSK 1228.8 kbps, 2 slots, 160AM 1843.2 kbps, 1 slot, 8PSK 2457.6 kbps, 1 slot, 160AM Data rates of reverse data/ 9.6 kbps traffic channel 19.2 kbps 38.4 kbps 76.8 kbps

153.6 kbps

Assistant functions to facilitate operation

Multichannel edit (forward link MAC RPC)	Common configuration of MAC RPC channels of a BS. Selectable parameters: – range of MAC RPC channels to be set – gain and gain step – data pattern – state
Copy BS/MS	Adopting the configuration of a BS/MS for another BS/MS to define multi-BS/ MS scenarios.

Digital standard IEEE 802.11(a,b,g) Wireless LAN (with option R&S SMU-K19/SMIQK19/AMIQK19)

Simulation of signals to Wireless LAN standard IEEE 802.11, available as Software Option R&S SMU-K19 and Option R&S SMU-B10 of R&S SMU or Software Option R&S SMIQK19 and Option R&S SMIQB60 Arbitrary Waveform Generator of R&S SMIQ or Software Option R&S AMIQK19 of R&S AMIQ

The wireless LAN options R&S SMU-K19 / SMIQK19 / AMIQK19 support CCK modulation to IEEE 802.11b and 802.11g, OFDM modulation to IEEE 802.11a and 802.11g as well as extended PBCC modes to IEEE 802.11b and 802.11g.

Settings valid for 11b, 11g and 11a

General settings	
Simulation mode	framed mode: generation of a se- quence of data packets with the frame structure defined by the standard, in- terrupted by an idle time unframed mode: generation of a non-packet-ori- ented signal without frame structure, with the modulation modes and data rates defined by the 802.11 standard
User data	same as with single-carrier system
Parameters in framed mode	
Idle time	time between two successive packets (PPDUs) in $\mu s;$ range 0 μs to 10000 μs
Number of packets	number of data packets to be generat- ed; the minimum number of packets is 1; the maximum number depends on the packet length, idle time and over- sampling
MAC header	activating and configuring the MAC header with the parameters Frame Control, Duration/ID, Address 1 to 4 and Sequence Control
Frame check sequence	activating or deactivating a 32 bit (4 byte) check sum for protecting the MAC header and the user data (frame body)

Settings for CCK (11b /11g)

General settings	
Chip rate Standard Range	11 Mcps see clock rates of R&S SMU-B10/ R&S SMIQB60/R&S AMIQ in the corre- sponding datasheets
Baseband filter Standard Other filters	Gaussian , B × T = 0.3 same as with single-carrier system
Parameters in framed mode	
PLCP preamble and header format	long PLCP and short PLCP
PSDU bit rate	1 Mbps, 2 Mbps, 5,5 Mbps and 11 Mbps
PSDU modulation	DBPSK, DQPSK and CCK (depending on specified PSDU bit rate)
PSDU data length	length of user data field in bytes of the packet to be transferred, range 0 byte to 4095 byte
Scrambling	data packet scrambling can be activat- ed or deactivated
Power Time Template	automatic configuration of power ramping at the beginning and end of the data packets with shiftable start points of the ramps relative to the data packet
Parameters in unframed mode	
PSDU bit rate	1 Mbps, 2 Mbps, 5.5 Mbps and 11 Mbps
PSDU modulation	DBPSK, DQPSK and CCK (depending on PSDU bit rate)

	PSDU bit rate)
Sequence length	length of signal to be generated in bytes, maximum length depending on oversampling
Scrambling	activating or deactivating scrambling

Settings for OFDM (11a /11g)

General settings

Kernel sample rate Standard Range	20 Msps see clock rates of R&S SMU-B10/ R&S SMIQB60/R&S AMIQ in the corre- sponding datasheets
Baseband filter Standard Other filters	ideal lowpass with Hanning window same as with single-carrier system
Parameters in framed mode	
PLCP Preamble	predefined according to 802.11a
PLCP SIGNAL field	automatically calculated
PSDU bit rate	6, 9, 12, 18, 24, 36, 48 and 54 Mbps
PSDU modulation	BPSK, QPSK, 16QAM or 64QAM, auto- matically set depending on specified PSDU bit rate
PSDU data length	length of user data field in bytes of the packet to be transferred, range 0 byte to 4095 byte, directly pro- portional to number of data symbols
Number of data symbols	number of OFDM symbols in data por- tion of packet, directly proportional to PSDU data length

Scrambling	data packet scrambling can be activat- ed or deactivated; initial scrambler state can be set randomly or to a user- defined value
Interleaver	interleaver can be activated or deactivated
Time domain windowing	time domain windowing supported with transition times between 0 ns and 1000 ns
Service field	user-defined service field value sup- ported
Parameters in unframed mode	
PSDU bit rate	6, 9, 12, 18, 24, 36, 48 and 54 Mbps
PSDU modulation	BPSK, QPSK, 16QAM or 64QAM, auto- matically set depending on specified PSDU bit rate
PSDU data length	data length in bytes to be transferred, range 0 byte to 2312 byte, directly pro- portional to number of data symbols
Number of data symbols	number of OFDM symbols to be gener- ated, directly proportional to PSDU data length
Scrambling	data scrambling can be activated or de- activated; initial scrambler state can be set randomly or to a user-defined value
Interleaver	interleaver can be activated or deacti- vated
Time domain windowing	time domain windowing supported with transition times between 0 ns and 1000 ns
Service field	user-defined service field value sup- ported
Settings for PBCC (11b /11g)	
General settings	
Chip rate Standard Range	11 Mcps see clock rates of R&S SMU-B10/ R&S SMIQB60/R&S AMIQ in the corre- sponding datasheets
Baseband filter Standard Other filters	Gaussian , B \times T = 0.3 same as with single-carrier system
Parameters in framed mode	
PLCP preamble and header format	long PLCP and short PLCP
PSDU bit rate	1 Mbps, 2 Mbps, 5,5 Mbps, 11 Mbps, 22 Mbps and 33 Mbps
PSDU modulation	DBPSK, DQPSK and PBCC (depending on specified PSDU bit rate)
PSDU data length	length of user data field in bytes of the packet to be transferred, range 0 byte to 4095 byte
Scrambling	data packet scrambling can be activat- ed or deactivated
Power Time Template	automatic configuration of power ramping at the beginning and end of the data packets with shiftable start points of the ramps relative to the data packet

Parameters in unframed mode

PSDU bit rate	1 Mbps, 2Mbps, 5,5 Mbps, 11 Mbps, 22 Mbps and 33 Mbps
PSDU modulation	DBPSK, DQPSK and PBCC (depending on specified PSDU bit rate)
Sequence length	length of signal to be generated in bytes, maximum length depending on oversampling
Scrambling	data scrambling can be activated or de- activated

Import system

Import of I/Q data from other applications via a dynamic data exchange (DDE) interface or via TCP/IP; further processing in R&S WinIQSIMTM, e.g. baseband filtering or superposition of impairments

Baseband filtering	same as with single-carrier system
Sequence length	R&S SMU-B10: 1 sample to max. 56 Msample R&S SMIQB60: 1 sample to max. 524.216 sample R&S AMIQO4: 1 sample to max. 16 Msample
Simulation of impairments and transfer characteristics	same as with single-carrier system
Smoothing	same as with single-carrier system
Graphical output	same as with single-carrier system
ACP calculation	calculation of adjacent-channel power in spectrum display (ACP up, low and ACP up 1st alt, low 1st alt)
IF signal generation	modulation of calculated I/Q signal to IF in range 0.01 MHz to 25 MHz (output to I channel of R&S AMIQ)

Miscellaneous

Waveform transmission to R&S AMIQ	interfaces: IEC/IEEE bus (GPIB), RS-232-C, floppy; conversion of I/Q signal to 14 bit R&S AMIQ format: user- selectable clipping level (over- and underranging possible)
Remote control of R&S AMIQ	download and starting of waveforms, hardware configuration, alignment and fine adjustment, file management, BER test
For data transfer	IEC/IEEE bus (GPIB): card (from National Instruments) with drivers, IEC/IEEE bus cable; RS-232-C null- modem cable

PC compatible to industry standard,
CPU clock min. 100 MHz, Windows95/
98 with 32 Mbyte RAM or Windows NT
with 48 Mbyte RAM recommended, at
least 50 Mbyte hard disk memory,
mouse, monitor: 1024×768 pixels
with 256 colours recommended

Ordering information

Baseband Generator with ARB (56 Msample) and Digital Modulation (realtime) Internal Arbitrary Waveform Generator in the R&S SMIQ I/Q Modulation Generator, 16 Msample	R&S SMU-B10	1141.7007.02
	R&S SMIQB60	1136.4390.02
	R&S AMIQ	1110.2003.04
Digital Standard IS-95 (cdmaOne)	R&S SMU-K11 R&S SMIQK11 R&S AMIQK11	1160.5335.02 1105.0287.02 1122.2003.02
cdma2000	R&S SMU-K12 R&S SMIQK12 R&S AMIQK12	1160.5658.02 1105.0435.02 1122.2503.02
1xEV-DO	R&S SMU-K17 R&S SMIQK17 R&S AMIQK17	1160.7009.02 1154.7800.02 1122.3000.02
3GPP TDD	R&S SMU-K13 R&S SMIQK13 R&S AMIQK13	1160.5906.02 1105.1231.02 1122.2603.02
TD-SCDMA	R&S SMU-K14 R&S AMIQK14 R&S SMIQK14	1160.6202.02 1122.2703.02 1105.1383.02
IEEE802.11 (a,b,g)	R&S SMU-K19 R&S SMIQK19 R&S AMIQK19	1160.8805.02 1154.8307.02 1122.3200.02
OFDM Signal Generation	R&S SMU-K15 R&S AMIQK15 R&S SMIQK15	1160.6402.02 1122.2803.02 1105.1531.02







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